

REMARKS

Summary of the Office Action

Claims 5, 6 and 18-20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Nanno et al.* (WO02/35507) in view of *Moon* (US 6,480,180).

Applicants wish to thank the Examiner for the courtesies extended, and for the helpful discussion provided, during the Interview conducted on October 19, 2007.

Summary of the Response to the Office Action

Applicants have amended claims 5, 6, and 18-20. Applicants have added new claims 22-31. Accordingly, claims 5, 6, 18-20, and 22-31 are pending for further consideration.

Summary of the Interview

With regard to the rejections of claims 5, 6 and 18-20 under 35 U.S.C. § 103(a) as being unpatentable over *Nanno et al.* in view of *Moon*, Applicants pointed out to the Examiner that the voltage VDD (1.8V) of *Nanno et al.* is not supplied to the digital circuit devices for processing digital signal, as required by independent claim 5.

In addition, Applicants further explained how the proposed claims 22 and 27 are different from the embodiments in *Nanno et al.* In particular, Applicants explained that the second power source voltage of claim 22 and the first power source voltage of claim 22 are processing voltages while the third power source voltages of claim 22 and the second power source voltages of claim 27 are reference voltages, and asserted that the voltages disclosed in *Nanno et al.* are merely reference voltages, i.e., not processing voltages, as claimed. Accordingly, Applicants asserted

that *Nanno et al.* is completely silent as to a voltage being used to process digital signal of the digital circuit devices, as claimed.

The Examiner indicated that the embodiment of *Nanno et al.* is distinguishable from the present invention and would be allowable over the applied references if the claimed language indicates that a DC-DC converter, which is admitted by the Examiner to correspond to the circuit in FIG. 4 of *Nanno et al.*, is not included in the digital circuit devices as claimed. In addition, the Examiner suggested introducing a first power source voltage before the introduction of the second power source voltage in claims 22 and 27, to render the claims allowable.

Accordingly, Applicants further amend the claims herein according to the Examiner's helpful comments.

The Rejection Under 35 U.S.C. § 103(a)

Claims 5-6 and 18-20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Nanno et al.* (WO02/35507) in view of *Moon* (US 6,480,180). Applicants respectfully traverse the rejection for at least the following reasons.

Independent claim 5, recites "taking a power source voltage less than 3.0V from a system; and supplying the power source voltage to digital circuit devices including a data driving circuit and a gate driving circuit for processing digital signal with respect to a reference voltage provided from a DC-DC converter," (emphasis added).

The Office Action suggests that "VDD (1.8V)" in FIG. 4 of *Nanno et al.* is the "power source voltage less than 3.0V," as claimed. FIG. 2 and Col. 8, lines 53-54 of *Nanno et al.* discloses that VDD (1.8V) is a reference power supply voltage supplied to a power supply

circuit, which generates voltages V1 (3.6V), V2 (10.8V), and V3 (-3.6V). Applicants respectfully assert that VDD (1.8V) is not supplied to the digital circuit devices including a data driving circuit and a gate driving circuit for processing digital signal, as required by independent claim 5.

Therefore, Applicants respectfully assert that the rejection under § 103(a) should be withdrawn because *Nanno et al.* and *Moon*, whether taken singly or combined, do not teach or suggest each feature of independent claim 5. Furthermore, Applicants respectfully assert that independent claim 18 is allowable for similar reasons and that dependent claims 6, 19, and 20 are allowable at least because of their dependence from the independent claims.

New Claims 22-31

Applicants have added new claims 22-31 to further define the invention. Applicants respectfully submit that new claims 22-31 are allowable at least because of the features recited therein.

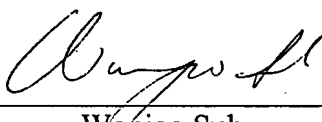
Conclusion

In view of the foregoing, Applicants respectfully request reconsideration and the timely allowance of all pending claims. Should the Examiner feel that there are any issues outstanding after consideration of this response, the Examiner is invited to contact Applicants' undersigned representative to expedite prosecution.

If there are any other fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-0310.

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

By: 

Wonjoo Suh
Reg. No. L0296

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MORGAN, LEWIS & BOCKIUS LLP
1111 Pennsylvania Avenue, N.W.
Washington, D.C. 20004
(202) 739-3000